

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1311	(test\$4 or check\$3 or verification or evaluation) same (wafer with (transistor or circuit)) same (location or position)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/08/24 17:30
L2	533	(test\$4 or check\$3 or verification or evaluation) same (wafer with (transistor or circuit)) same (paramet\$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/08/24 17:31
L3	2	(test\$4 or check\$3 or verification or evaluation) same (wafer with (transistor or circuit)) same ((connect\$3 or hook-up or hook or attach\$5) with (lithographic or litho-grahic))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/08/24 17:33
L4	1	1 and 2 and 3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/08/24 17:33
L5	138	1 and 2	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/08/24 17:33
L6	17	5 and (lithographic or litho-grahic)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/08/24 17:45
L7	29	5 and (lithographic or litho-grahic or photolithographic or photolitho-grahic)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/08/24 17:46